

특허 협력 조약  
(PATENT COOPERATION TREATY)

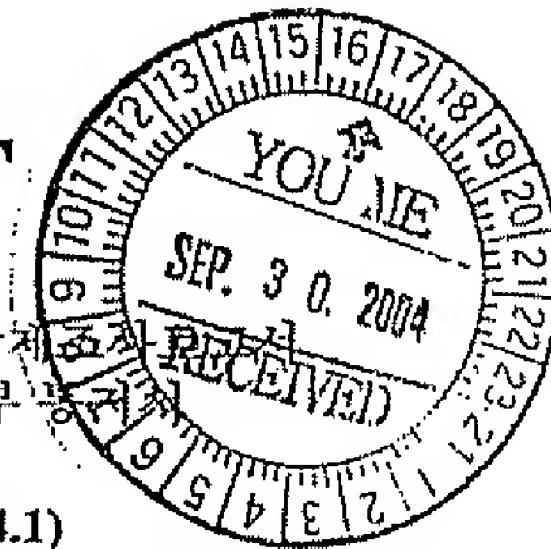
발신: 국제조사기관

수신:

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Teheran Bldg., 825-33 Yoksam-dong, Kangnam-ku,  
Seoul 135-080, Republic of Korea

PCT



국제조사보고서 또는 국제출원  
부작성 선언서 송부

(PCT 규칙 44.1)

발송일  
(일/월/년)

23 SEPTEMBER 2004 (23.09.2004)

출원인 또는 대리인 서류참조번호

OPP031986KR

이후의 절차에 대하여는 아래 1. 및 4. 참조

국제출원번호

PCT/KR2003/002897

국제출원일  
(일/월/년)

30 DECEMBER 2003 (30.12.2003)

출원인

LUXPIA CO., LTD. et al

1.  국제보고서가 작성되어 이를 송부함을 출원인에게 통지합니다.

조약 제19조의 규정에 의한 보정서 및 설명서의 제출:

희망하는 경우 출원인은 국제출원의 청구의 범위를 보정할 수 있습니다 (조약규칙 46):

기간 보정서 제출기간은 통상 국제조사보고서 송부일부터 2월.

제출처 The international Bureau of WIPO  
34, chemin des Colmbelets  
1211 Geneva 20, Switzerland  
Faxcimile No.: (41-22) 740 14 35

보다 자세한 자침에 대하여는 첨부되는 용지에 기재된 설명을 참조.

2.  국제조사보고서가 작성되지 아니할 것이며 조약 제17조(2)(a)의 규정에 의한 선언서가 송부됨을 출원인에게 통지합니다.

3.  조약규칙 40.2의 규정에 의한 추가수수료 납부에 대한 이의신청과 관련하여 아래 사항을 출원인에게 통지합니다.

이의신청 및 이의신청에 대한 결정 통지서를 출원인이 제출한 이의신청 및 이의신청에 대한 결정 통지서의 지정관청에의 송부 신청서와 함께 국제사무국에 송부하였습니다.

이의신청에 대한 결정이 아직 이루어지지 않았습니다; 결정이 이루어지는 대로 출원인에게 통지할 것입니다.

4. 주의사항

우선일부터 18월 이후 국제사무국은 국제출원을 공개합니다. 공개를 하지 않거나 또는 연기할 것을 희망하는 경우에는 출원인은 국제공개의 기술적 준비가 완료되기 전에 각각의 경우에 따라 조약 규칙 90bis.1 및 90bis.3의 규정에 의한 국제출원 취하서 또는 우선권주장 취하서를 국제사무국에 제출하여야 합니다.

일부 지정관청에 대하여는 출원인이 우선일부터 30일까지 (일부관청에 대하여는 더 늦을 수도 있음) 국내단계의 개시를 연기하고자 하는 경우에는 우선일부터 19월 이내에 국제예비심사를 청구하여야 합니다; 그렇지 않은 경우 출원인은 우선일부터 20월 이내에 당해 지정관청에 대한 국내단계의 개시를 위한 규정된 절차를 밟아야 합니다.

다른 지정관청에 대하여는, 우선일부터 19월 이내에 국제예비심사를 청구하지 않더라도 30월(또는 더 늦을 수도 있음)의 기간이 적용될 것입니다.

서식 PCT/IB/301의 부속서류를 참조하기 바라며, 각 특허청마다의 적용기간에 대한 구체적인 사항은 PCT출원인 안내 제2권 국내단계 및 WIPO 인터넷 사이트를 참조하시기 바랍니다.

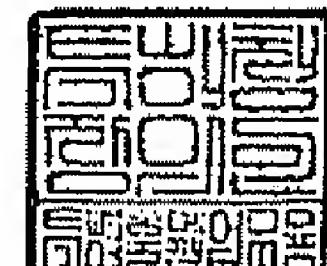
ISA/KR의 명칭 및 우편주소



대한민국 특허청  
302-701 대한민국 대전광역시 서구 둔산동  
920번지 정부대전청사

팩스 번호 82-42-472-7140

특허청장



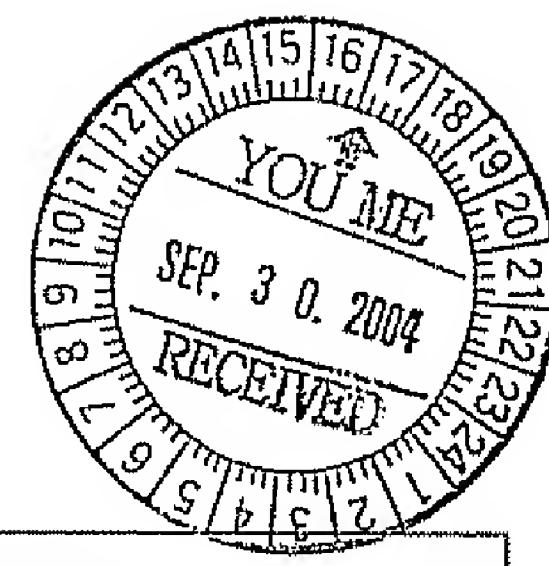
전화번호 82-42-481-5281

## PATENT COOPERATION TREATY

PCT

## INTERNATIONAL SEARCH REPORT

(PCT Article 18 and Rules 43 and 44)



Applicant's or agent's file reference OPP031986KR	FOR FURTHER ACTION		see Form PCT/ISA/220 as well as, where applicable, item 5 below.
International application No. <b>PCT/KR2003/002897</b>	International filing date (day/month/year) <b>30 DECEMBER 2003 (30.12.2003)</b>	(Earliest) Priority Date (day/month/year) <b>23 DECEMBER 2003 (23.12.2003)</b>	
Applicant <b>LUXPIA CO., LTD. et al</b>			

This International search report has been prepared by this International Searching Authority and is transmitted to the applicant according to Article 18. A copy is being transmitted to the International Bureau.

This international search report consists of a total of 3 sheets.

It is also accompanied by a copy of each prior art document cited in this report.

1. Basis of the report
  - a. With regard to the language, the international search was carried out on the basis of the international application in the language in which it was filed, unless otherwise indicated under this item.
 

The international search was carried out on the basis of a translation of the international application furnished to this Authority (Rule 23.1(b)).
  - b.  With regard to any nucleotide and/or amino acid sequence disclosed in the international application, see Box No. I.
2.  Certain claims were found unsearchable (See Box No. II)
3.  Unity of invention is lacking (See Box No. III)
4. With regard to the title,
 

the text is approved as submitted by the applicant.

the text has been established by this Authority to read as follows:
5. With regard to the abstract,
 

the text is approved as submitted by the applicant.

the text has been established, according to Rule 38.2(b), by this Authority as it appears in Box No. IV. The applicant may, within one month from the date of mailing of this international search report, submit comments to this Authority.
6. With regard to the drawings,
  - a. the figure of the drawings to be published with the abstract is Figure No. 1

as suggested by the applicant.

because the applicant failed to suggest a figure.

because this figure better characterizes the invention.
  - b.  none of the figure is to be published with the abstract.

## INTERNATIONAL SEARCH REPORT

International application No. PCT/KR2003/002897
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## A. CLASSIFICATION OF SUBJECT MATTER

**IPC7 H01L 33/00, C09K 11/08**

According to International Patent Classification (IPC) or to both national classification and IPC

## B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

IPC 7 H01L C09K H01J

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched  
Korean Patents and applications for inventions, since 1973

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

## C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	US 4755715 ( AMERICAN TELEPHONE & TELEGRAPHY COMPANY, AT&T BELL LABORATORIES ) 05 JULY 1988 see the whole documents	1-25
A	US 6066861 ( SIEMENS AKTIENGESELLSCHAFT ) 23 MAY 2000 see the whole documents	1-25
A	US 6614179 B1 ( NICHIA KAGAKU KOGYO KABUSHIKI KAISHA ) 02 SEPTEMBER 2003 see the whole documents	1-25
A	US 6522065 B1 ( GENERAL ELECTRIC COMPANY ) 18 FEBRUARY 2003 see the whole documents	1-25
A	US 4539506 ( INTERNATIONAL BUSINESS MACHINES CORPORATION ) 13 SEPTEMBER 1985 see the whole documents	9-10, 16, 19, 23

 Further documents are listed in the continuation of Box C. See patent family annex.

* Special categories of cited documents: "A" document defining the general state of the art which is not considered to be of particular relevance "E" earlier application or patent but published on or after the international filing date "L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of citation or other special reason (as specified) "O" document referring to an oral disclosure, use, exhibition or other means "P" document published prior to the international filing date but later than the priority date claimed	"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention "X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone "Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art "&" document member of the same patent family
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Date of the actual completion of the international search  
20 SEPTEMBER 2004 (20.09.2004)

Date of mailing of the international search report

**21 SEPTEMBER 2004 (21.09.2004)**

Name and mailing address of the ISA/KR

Korean Intellectual Property Office  
920 Dunsan-dong, Seo-gu, Daejeon 302-701,  
Republic of Korea

Facsimile No. 82-42-472-7140

Authorized officer

KIM, Dong Yup

Telephone No. 82-42-481-5749



**INTERNATIONAL SEARCH REPORT**  
Information on patent family members

International application No.  
PCT/KR2003/002897

Patent document cited in search report	Publication date	Patent family member(s)	Publication date
US 4755715	05.07.1988	NONE	
US 6066861	23.05.2000	US 6277301 BA US 2001045647 AA US 2001028053 AA US 6245259 BA US 2004084687 AA US 6613247 BA US 6592780 BB US 2004016908 AA	21.08.2001 28.11.2001 11.10.2001 12.06.2001 06.05.2004 02.09.2003 15.07.2003 29.01.2004
US 6614179 B1	02.09.2003	US 2001001207 AA US 2004090180 AA US 6608332 BB US 2004004437 AA	17.05.2001 13.05.2004 19.08.2003 08.01.2004
US 6522065 B1	18.02.2003	EP 1138747 A2 CN 1315484 A JP 2002076446 A2 EP 1138747 A3 TW 522423 B US 2003067265 AA	04.10.2001 03.10.2001 15.03.2002 02.07.2003 01.03.2003 10.04.2003
US 4539506	13.09.1985	NONE	

From the INTERNATIONAL BUREAU

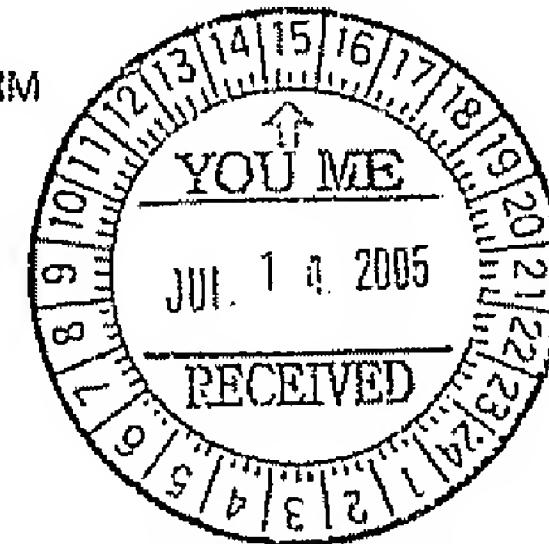
## PCT

NOTICE INFORMING THE APPLICANT OF THE  
COMMUNICATION OF THE INTERNATIONAL  
APPLICATION TO THE DESIGNATED OFFICES

(PCT Rule 47.1(c), first sentence)

To:

YOU ME PATENT & LAW FIRM  
Seolim Bldg.  
649-10 Yoksam-dong  
Kangnam-ku  
Seoul 135-080  
RÉPUBLIQUE DE CORÉE



Date of mailing (day/month/year)  
07 July 2005 (07.07.2005)

Applicant's or agent's file reference  
OPP031986KR

## IMPORTANT NOTICE

International application No. PCT/KR2003/002897	International filing date (day/month/year) 30 December 2003 (30.12.2003)	Priority date (day/month/year) 23 December 2003 (23.12.2003)
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Applicant LUXPIA CO., LTD. et al
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1. Notice is hereby given that the International Bureau has communicated, as provided in Article 20, the international application to the following designated Offices on the date indicated above as the date of mailing of this notice:

AU, AZ, BY, CH, CN, CO, DZ, EP, HU, KG, KP, MD, MK, MZ, RU, SY, TM, US

In accordance with Rule 47.1(c), third sentence, those Offices will accept the present notice as conclusive evidence that the communication of the international application has duly taken place on the date of mailing indicated above and no copy of the international application is required to be furnished by the applicant to the designated Office(s).

2. The following designated Offices have waived the requirement for such a communication at this time:

AE, AG, AL, AM, AP, AT, BA, BB, BG, BR, BW, BZ, CA, CR, CU, CZ, DE, DK, DM, EA, EC, EE, EG, ES, FI, GB, GD, GE, GH, GM, HR, ID, IL, IN, IS, JP, KE, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MG, MN, MW, MX, NI, NO, NZ, OA, OM, PG, PH, PL, PT, RO, SC, SD, SE, SG, SK, SL, TJ, TN, TR, TT, TZ, UA, UG, UZ, VC, VN, YU, ZA, ZM, ZW

The communication will be made to those Offices only upon their request. Furthermore, those Offices do not require the applicant to furnish a copy of the international application (Rule 49.1(a-bis)).

3. Enclosed with this notice is a copy of the international application as published by the International Bureau on  
07 July 2005 (07.07.2005) under No. WO 2005/062391

4. TIME LIMITS for filing a demand for international preliminary examination and for entry into the national phase

The applicable time limit for entering the national phase will, subject to what is said in the following paragraph, be 30 MONTHS from the priority date, not only in respect of any elected Office if a demand for international preliminary examination is filed before the expiration of 19 months from the priority date, but also in respect of any designated Office, in the absence of filing of such demand, where Article 22(1) as modified with effect from 1 April 2002 applies in respect of that designated Office. For further details, see *PCT Gazette* No. 44/2001 of 1 November 2001, pages 19926, 19932 and 19934, as well as the *PCT Newsletter*, October and November 2001 and February 2002 issues.

In practice, time limits other than the 30-month time limit will continue to apply, for various periods of time, in respect of certain designated or elected Offices. For regular updates on the applicable time limits (20, 21, 30 or 31 months, or other time limit), Office by Office, refer to the *PCT Gazette*, the *PCT Newsletter* and the *PCT Applicant's Guide*, Volume II, National Chapters, all available from WIPO's Internet site, at <http://www.wipo.int/pct/en/index.html>.

For filing a demand for international preliminary examination, see the *PCT Applicant's Guide*, Volume IVA, Chapter IX. Only an applicant who is a national or resident of a PCT Contracting State which is bound by Chapter II has the right to file a demand for international preliminary examination (at present, all PCT Contracting States are bound by Chapter II).

It is the applicant's sole responsibility to monitor all these time limits.

The International Bureau of WIPO  
34, chemin des Colombettes  
1211 Geneva 20, Switzerland

Authorized officer

Philippe Becamel

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## (12) INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(19) World Intellectual Property Organization International Bureau



(43) International Publication Date  
7 July 2005 (07.07.2005)

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(10) International Publication Number  
WO 2005/062391 A1

(51) International Patent Classification<sup>7</sup>: H01L 33/00, C09K 11/08

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23 December 2003 (23.12.2003) KR

(71) Applicant (for all designated States except US): LUX-PIA CO., LTD. [KR/KR]; Jeonju Science Industrial Park, 948-1 Dunsan-ri, Bongdong-eup, 565-902 Wanju-city, Jeollabuk-do (KR).

(72) Inventors; and

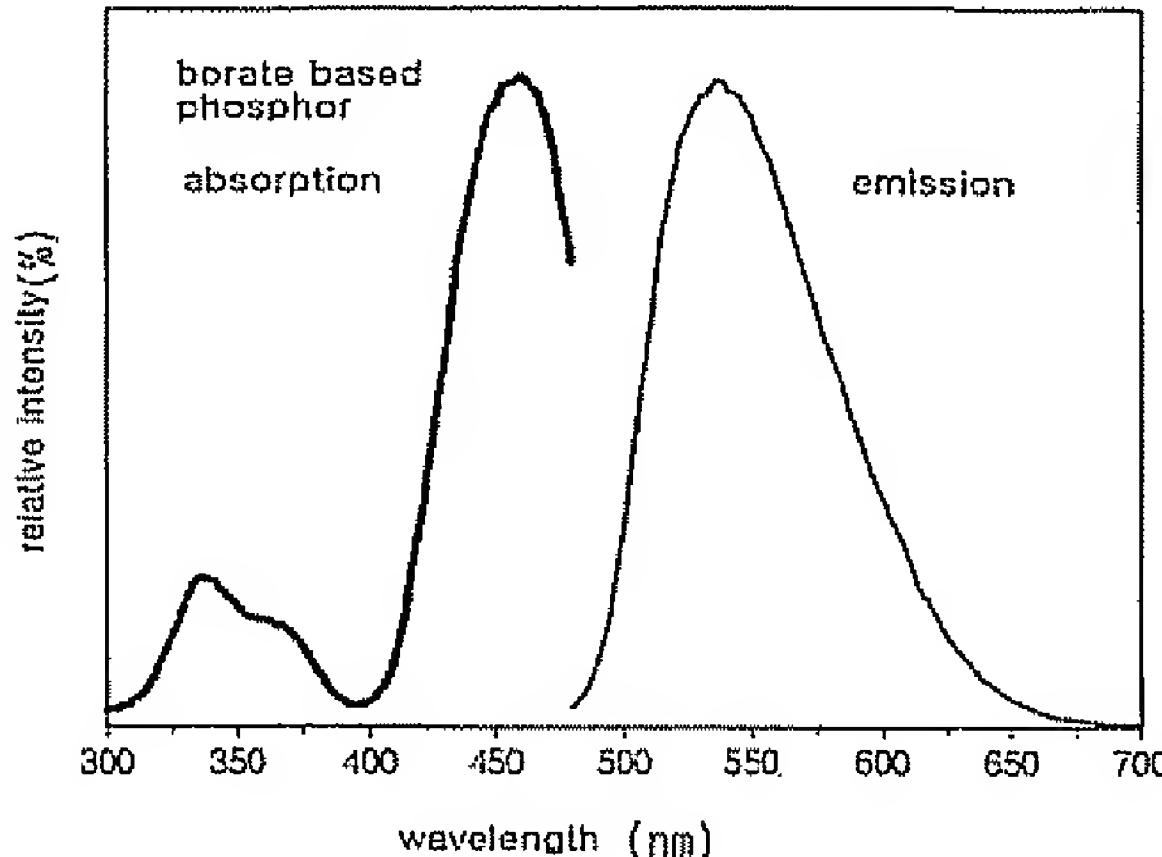
(75) Inventors/Applicants (for US only): LEE, Dong-Yeon [KR/KR]; 11/2, 540 Jukdong-dong, Gangseo-gu, 618-320 Busan-city (KR). KIM, Yong-Tae [KR/KR]; 103-509

(52) Designated States (national): AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BW, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, BG, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NI, NO, NZ, OM, PG, PH, PL, PT, RO, RU, SC, SD, SE, SG, SK, SL, SY, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, YU, ZA, ZM, ZW.

(53) Designated States (regional): ARIPO patent (BW, GH, GM, KE, LS, MW, MZ, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HU, IE, IT, LU, MC, NL, PT, RO, SE, SI, SK, TR), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).

[Continued on next page]

(54) Title: YELLOW EMITTING PHOSPHOR AND WHITE SEMICONDUCTOR LIGHT EMITTING DEVICE INCORPORATING THE SAME



(57) Abstract: The present invention relates to a yellow phosphor represented by a general formula of  $A_{(1-x)}D_{5-x}E_xO_{12}:Ce_y$  (wherein: A is at least one element selected from the group consisting of Y, Lu, Sc, La, Gd and Sm; D is at least one element selected from the group consisting of Al, Ga and In; E is at least one element selected from the group consisting of B and Fe;  $0 \leq x < 0.5$ ; and  $0.0001 \leq y < 0.5$ ), and a white semiconductor light emitting device incorporating the same. The white semiconductor light emitting device comprises a semiconductor light emitting diode, and a phosphor coating layer. The phosphor coating layer comprises yellow phosphor and optionally zinc selenium-based red phosphor that absorb a portion of light emitted by the semiconductor light emitting diode and emits light of wavelength different from that of the absorbed light and a transparent resin. The white semiconductor light emitting device has excellent color rendering and experiences only extremely low degrees of deterioration in emission efficiency in a long period of service.

WO 2005/062391 A1



**Published:**

— *with international search report*

*For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette*

TITLE OF THE INVENTIONYELLOW EMITTING PHOSPHOR AND WHITE SEMICONDUCTOR LIGHT  
EMITTING DEVICE INCORPORATING THE SAME5 BACKGROUND OF THE INVENTION

## (a) Field of the Invention

The present invention relates to a semiconductor light emitting device. More particularly, it relates to a yellow phosphor and a semiconductor light emitting device incorporating the yellow phosphor which absorbs a portion of light emitted by a light emitting diode, and emits light of a wavelength different from that of the absorbed light, thereby implementing such white light as purely white light and bluish white light by incorporating the yellow phosphor.

## (b) Description of the Related Art

A semiconductor light emitting diode (LED) is a PN-junctioned compound semiconductor. It is a kind of optoelectronic device that emits light energy corresponding to the band gap of a semiconductor generated by a combination of an electron and a hole when a voltage is applied.

As full colorization of an LED was realized with the development of a high luminance blue LED using a GaN –based nitride as the semiconductor luminescent material, application of LEDs has expanded from display devices to illumination devices). LEDs for lighting applications offer about 10 to 15% less power consumption compared with conventional illumination devices such as fluorescent bulbs and incandescent bulbs, a semi-permanent life of over 100,000 hours, and

environmental friendliness. Thus, they can significantly improve energy efficiency.

For using a semiconductor luminescent element for an illumination device, white light should be obtainable using LEDs. Largely, three methods of fabricating white semiconductor light emitting diode have been used. One of them obtains 5 white light by combining three LEDs emitting red, green and blue colors, respectively. In this method, an InGaN or AlInGaN phosphor is used as a luminescent material. According to this method, it is difficult to construct a white LED by combining three RGB LEDs on a single chip, and also to control a current strength because each LED is made from different material by different method, and 10 the driving voltage of each LED is different. In another method, a UV LED is used as a light source to excite three-color (RGB) phosphor to obtain white light. It uses an InGaN/R,G,B phosphor as a luminescent material. This method is applicable under a high current and improves color sensation. However, the above two methods have the following problems: a suitable material to obtain green light 15 has not been developed as yet; and light emitted from the blue LED may be absorbed by the red LED to lower the overall light emitting efficiency. As an alternative method, a blue LED is used as a light source to excite a yellow phosphor to obtain white light. In general, an InGaN/YAG:Ce phosphor is used as a luminescent material in this method.

20 When the illumination device uses phosphor, its emitting efficiency increases as a difference in wavelengths of an exciting radiation and an emitted radiation gets small. Thus, the light emitting characteristic of a phosphor plays a very important role in determining the color and luminance of a semiconductor light emitting device when incorporated therein. Generally, a phosphor includes a 25 matrix made of a crystalline inorganic compound, and an activator that converts the

matrix into an effective fluorescent material. The phosphor emits light mainly in the visible wavelength region when an electron excited by absorbing a variety of forms of energy returns to its ground state. The color of emitted light can be adjusted by controlling the combination of the matrix and activator.

5 Examples of white semiconductor light emitting devices are disclosed in many documents.

USP Nos. 5,998,925 and 6,069,440 (Nichia Kagaku Kogyo Kabushiki Kaisha) disclose a white semiconductor light emitting device using a nitride semiconductor, which comprises a blue light emitting diode containing the nitride semiconductor represented by the formula:  $\text{In}_i\text{Ga}_j\text{Al}_k\text{N}$  ( $0 \leq i, 0 \leq j, 0 \leq k, i + j + k = 1$ ) and a yellow phosphor containing a YAG (yttrium, aluminum, garnet) -based garnet fluorescent material that absorbs a portion of light emitted from the blue light emitting diode and emits light of wavelength different from that of the absorbed light. For the YAG -based phosphor, a mixture of a first phosphor,  $\text{Y}_3(\text{Al}_{1-s}\text{Ga}_s)_5\text{O}_{12}:\text{Ce}$ , and a second phosphor,  $\text{RE}_3\text{Al}_5\text{O}_{12}:\text{Ce}$ , ( $0 \leq s \leq 1$ ; RE is at least one of Y, Ga and La) are used.

USP No. 6,504,179 (Osram Optosemiconductors GmbH) discloses a white-emitting illuminating unit using a BYG approach (combination of blue, yellow and green) instead of the conventional RGB approach (combination of red, green and blue) or BY approach (combination of blue and yellow). This white-emitting illumination unit comprises an LED emitting a first light in the range of 300 nm to 470 nm as a light source, and the first light is converted into light of longer wavelength by the phosphor exposed to the first light. To aid the conversion, an Eu-activated calcium magnesium chlorosilicate-based green phosphor and a Ce-activated rare earth garnet-based yellow phosphor are used. For the Ce-activated rare earth

garnet-based yellow phosphor, a phosphor represented by the formula  $RE_3(Al, Ga)_5O_{12}:Ce$  (RE is Y and/or Tb), at least 20 % of the total emission of which lies in the visible region of over 620 nm, is used.

USP No. 6,596,195 of General Electric discloses a phosphor which is 5 excitable between the near UV and blue wavelength region (ranging from about 315 nm to about 480 nm) and has an emission peak between the green to yellow wavelength region (ranging from about 490 nm to about 770 nm), and a white light source incorporating the same. This phosphor has a garnet structure and is represented by the formula:  $(Tb_{1-x-y}A_xRE_y)_3D_zO_{12}$  (where: A is selected from the 10 group consisting of Y, La, Gd and Sm; RE is selected from the group consisting of Ce, Pr, Nd, Sm, Eu, Gd, Dy, Ho, Er, Tm, Yb and Lu; D is selected from the group consisting of Al, Ga and In; A is selected such that A is different from RE; x is in the range from 0 to 0.5; y is in the range from 0.0005 to 0.2; and z is in the range from 4 to 5).

15 As described above, conventional white semiconductor light emitting devices excite YAG -based yellow phosphors to emit light mainly using UV LEDs or blue LEDs and obtain white light with a combination thereof. However, the YAG-based yellow phosphor emits yellowish green light, and if other materials are added in place of yttrium and aluminum to cause a change in emitted light toward a 20 longer wavelength, the emitting luminance is reduced.

#### SUMMARY OF THE INVENTION

Thus, an object of the present invention is to solve the problems described above, and provide a phosphor that can improve the emitting luminance and color

rendering of a white light emitting device and a white semiconductor light emitting device which experiences only extremely low degrees of deterioration in emission intensity, emission efficiency and color shift over a long period of service and implements a wide range of colors.

5

BRIEF DESCRIPTION OF THE DRAWINGS

FIG.1 is a graph showing an absorption spectrum and emission spectrum of borate-based yellow phosphor in accordance with one embodiment of the present invention.

10 FIG.2 is a graph showing an emission spectrum depending on the amount of boron in borate-based yellow phosphor in accordance with one embodiment of the present invention.

15 FIG.3 is a graph showing an emission spectrum of white light emitting diode combining a blue LED with borate-based yellow phosphor in accordance with one embodiment of the present invention.

FIG.4 is a graph showing absorption spectrum and emission spectrum of zinc selenium-based red phosphor in accordance with another embodiment of the present invention.

20 FIG.5 is a graph showing emission spectrum of pink light emitting diode combined blue LED with zinc selenium-based red phosphor in accordance with another embodiment of the present invention.

FIG.6 is a color coordinate representing the range of color reproduction

obtained by the light emitting diode combined blue LED with borate-based yellow phosphor and zinc selenium-based red phosphor in accordance with another embodiment of the present invention.

FIG.7 is a schematic view of a lead type white semiconductor light emitting device incorporating borate-based yellow phosphor and zinc selenium-based red phosphor in accordance with the present invention, and a partial enlarged sectional view thereof.

FIG.8 is a schematic view of a double mold type white semiconductor light emitting device, and a partial enlarged sectional view thereof.

FIG.9 is a schematic view of a surface mount type white light emitting device of the reflector injection type incorporating borate-based yellow phosphor and zinc selenium-based red phosphor in accordance with another embodiment of the present invention.

FIG.10 is a schematic view of a surface mount type white light emitting device of the reflector injection and double mode structure.

FIG.11 is a sectional view of a surface mount type white light emitting device of the PCB type incorporating borate-based yellow phosphor and zinc selenium-based red phosphor in accordance with another embodiment of the present invention.

\* Description of the reference number of in the drawings\*

3, 10, 20 : LED chip

4, 11, 22 : anode lead

5, 12, 21 : cathode lead

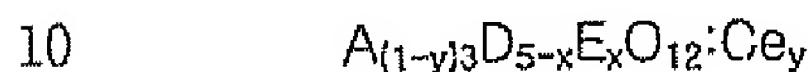
6, 6a, 13, 13a, 23 : phosphor coating layer  
6b, 13b: transparent material layer 8: phosphor particle  
9, 17 : recess portion 16: casing  
25: PCB layer 15, 26: molding layer

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DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS

In one aspect, the present invention provides a yellow phosphor represented by the following chemical formula 1:

Chemical formula 1



In the formula, A is at least one element selected from the group consisting of Y, Lu, Sc, La, Gd and Sm; D is at least one element selected from the group consisting of Al, Ga and In; E is at least one rare element selected from the group consisting of B and Fe;  $0 \leq x < 5$ ; and  $0.0001 \leq y < 0.5$ .

15 The shape of phosphor is not limited particularly, but preferably is polygonal, spherical, or flaked, and more preferably is spherical phosphor having a mean diameter of 100 nm to 50  $\mu$ m.

The yellow phosphor has an absorption peak at about 420 nm to 480 nm of wavelength, and an emission peak at about 510 nm to 570nm of wavelength.

20 In another aspect, the present invention further provides a white semiconductor light emitting device comprising a semiconductor light emitting diode, and a phosphor coating layer comprising a yellow phosphor, which absorbs a portion of light emitted from the semiconductor light emitting diode and emits light

of wavelength different from that of the absorbed light, and a transparent resin, wherein the yellow phosphor comprises a yellow phosphor represented by chemical formula 1.

In the semiconductor light emitting device, the main emission spectrum 5 peak lies in the range from 400 nm to 530 nm. Preferably, the main emission wavelength of the yellow phosphor is longer than the main peak wavelength of the semiconductor light emitting diode.

The thickness of the phosphor coating layer( $T_1$ ) and the thickness of the semiconductor light emitting device( $T_2$ ) meet preferably the equation,  $T_2 < T_1 \leq 10 3T_2$ , and more preferably  $1.5 T_2 < T_1 \leq 2.5T_2$ .

The shape of yellow phosphor in the phosphor coating layer is not limited, but is preferably polygonal, spherical, or flaked, and more preferably spherical, with a mean diameter of 0.1 to 50  $\mu\text{m}$ .

The yellow phosphor in phosphor coating layer contains preferably 0.01 to 15 10 wt% of phosphor particles having a mean diameter of less than 1  $\mu\text{m}$ , and 90 to 99.9 wt% of phosphor particles having a mean diameter of 1 to 50  $\mu\text{m}$ . The yellow phosphor can be a mixture of phosphor represented by chemical formula 1 and conventional yellow phosphors such as  $(\text{YGD})_3(\text{AlGa})_5\text{O}_{12}:\text{Ce}$ , or  $\text{Tb}_3\text{Al}_5\text{O}_{12}:\text{Ce}$ .

The phosphor coating layer further comprises zinc selenium (ZnSe)-based 20 red phosphor. Preferably, the zinc selenium-based red phosphor is mixed in the amount of 10 to 40 wt% based on the weight of the yellow phosphor.

The semiconductor light emitting device comprises a substrate and a nitride semiconductor layer. The substrate is made from sapphire ( $\text{Al}_2\text{O}_3$ ) or silicone

carbide (SiC), the nitride semiconductor layer comprises a GaN, InGaN, or InGaN semiconductor.

In another aspect, lead type white semiconductor light emitting device and surface mount type light emitting type white semiconductor light emitting device 5 with various structures are provided.

The white semiconductor light emitting device can be used for backlight in a liquid crystal device (LCD).

Hereinafter, the present invention is described in more detail.

The yellow phosphor provided by the present invention is represented by the 10 following chemical formula 1:

Chemical formula 1



In the formula, A is at least one element selected from the group consisting of Y, Lu, Sc, La, Gd and Sm; D is at least one element selected from the group 15 consisting of Al, Ga and In; E is at least one element selected from the group consisting of B and Fe;  $0 \leq x < 5$ ; and  $0.0001 \leq y < 0.5$ .

In the chemical formula, a desirable light emission efficiency, luminance, and good maintenance of activator function can be obtained when x and y are in the described ranges.

20 In the formula, A is a mixture of Y and Gd, the mixing mole ratio of which can be adjusted.

The yellow phosphor has an absorption peak at about 420 nm to 480 nm

and an emission peak at about 510 nm to 570nm.

The shape of phosphor is not limited, but preferably is polygonal, spherical, or flaked. More preferably, it is spherical phosphor having a mean diameter of 100 nm to 50  $\mu\text{m}$ , preferably 100nm to 30 $\mu\text{m}$ .

5 Examples of the yellow phosphor are  $\text{Y}_{2.99}\text{Al}_2\text{B}_3\text{O}_{12}:\text{Ce}_{0.01}$ ,  $\text{Y}_{2.99}\text{Al}_4\text{BO}_{12}:\text{Ce}_{0.01}$ , and  $(\text{Y}\text{Gd})_{2.99}\text{Al}_2\text{B}_3\text{O}_{12}:\text{Ce}_{0.01}$ .

The yellow phosphor according to the present invention can be prepared by several methods known to one of ordinary skilled in the art, including the solid phase method, liquid phase method, and gas phase method. Among the 10 preparation methods, the spray pyrolysis which is a kind of gas phase method is preferable to produce the nano-sized phosphor particles. When preparing the nano-sized yellow phosphor using the spray pyrolysis, the phosphor has improved 15 luminescent property despite of narrow particle distribution. This is what causes a surface defect of phosphor caused in the ball milling process to reduce the particle size of the solid phase method, to lower the luminance. Because phosphor particles prepared by spray pyrolysis are alone or are divided into several particles through re-crystallization, the luminance is not reduced. The phosphor particles prepared by ultrasonic spray pyrolysis have a mean diameter of 100 nm to 10  $\mu\text{m}$ .

In the preparation of phosphor powder according to the present invention, 20 the phosphor structure may become different depending on a metal compound for forming the phosphor matrix and a metal compound doped into the matrix. All modification of the component and its amount made by an ordinary skilled person in

the art fall within the present invention as long as the component is represented by chemical formula 1, and amount thereof falls with the scope of the present invention.

Preparation of a phosphor by the gas phase method will be described in 5 detail.

In the gas phase method, a phosphor is prepared by three steps of: (1) preparing a precursor solution by dissolving a nitrate compound of component element constituting the yellow phosphor represented by chemical formula 1, and boric acid or iron nitrate in a solvent; (2) supplying the precursor solution to a 10 spraying unit to form droplets; and (3) drying, pyrolyzing and heat treating the droplets using a spraying and pyrolyzing unit.

Each step of the preparation of a phosphor will be described as below.

< Step 1: Preparation of spray solution >

In preparing a precursor spray solution to obtain a phosphor powder, at 15 least one element selected from the group consisting of Y, Lu, Sc, La, and Gd, at least one element selected from the group consisting of Sm, Al, Ga and In, and boron compound, or iron compound, etc. are used for preparing a phosphor powder matrix, and a cerium compound is used for preparing an activator to dope into the matrix. Water or alcohol is used as a solvent to dissolve the metal 20 compounds for the phosphor matrix, and as the matrix metal compound, nitrates, acetates, chlorides, hydroxides or oxide forms that easily dissolve in the solvent are used.

Because the phosphor particle size is determined by the concentration of the precursor solution, the concentration of the precursor solution should be 25 controlled to obtain particles of a desirable size. Preferably, the concentration is

controlled in the range of 0.002 M to 3.0 M. If the concentration is below 0.002 M, the phosphor powder yield decreases. Otherwise, if it is over 3.0 M, the precursor solution is not sprayed well due to a solubility problem.

< Step 2: Spraying droplets >

5        The obtained precursor solution is supplied to a spraying unit and sprayed as liquid droplets. It is preferable that the diameter of the liquid drop lies in the range of from 1 to 10  $\mu\text{m}$  in consideration of the final phosphor particle size. For the spraying unit, an ultrasonic spraying unit, air nozzle spraying unit, ultrasonic nozzle spraying unit, etc. can be used. When an ultrasonic spraying unit is used, fine 10 phosphor powders of sub-micron dimension can be prepared in high concentration, and when air nozzle or ultrasonic nozzle units are used, particles of micron to sub-micron dimensions can be prepared in large scale. To obtain phosphor powders, an ultrasonic liquid drop generation unit, which produces fine liquid drops having a size of several microns, is preferable.

15        < Step 3: Preparation of phosphor powder >

Fine liquid drops formed by the liquid drop generation unit are converted to phosphor particle precursors in a hot tube reactor. Preferably, the temperature of the reaction electric furnace is maintained in the range from 200 to 1,500  $^{\circ}\text{C}$ , which is the range that the precursor materials can be dried and pyrolyzed. In the 20 spraying and pyrolyzing step, the liquid drops pass through the reactor within a few seconds. Therefore, heat treatment is performed for crystal growth of the phosphor particles. This heat treatment is performed at a temperature range of 800 to 1,800  $^{\circ}\text{C}$ , more preferably 1,100 to 1,300  $^{\circ}\text{C}$  for 1 to 20 hours. The heat treatment temperature may be varied depending on the phosphor.

FIG.1 is a graph showing an absorption spectrum and an emission spectrum of borate-based yellow phosphor. As shown in FIG.1, the yellow phosphor according to the present invention has a high absorption peak at 420 nm to 480 nm, and a high emission peak at 510 nm to 570 nm.

5 FIG.2 represents an emission spectrum depending on the different compositions of the matrix of the phosphor. When x is 1, 2, or 3 in  $Y_{2.99}Al_{5-x}B_xO_{12}:Ce_{0.01}$ , the phosphor has a high absorption peak at 400 nm to 470 nm, and height emission peak at around 530 nm. Such a result reported that the phosphor maintained a good emission property, even though the stoichiometrical  
10 ratio of boron increased.

Thus, the yellow phosphor can be used suitably for implementing white color by using a blue chip and applications adopting the light as an energy source.

FIG.3 represents an emission spectrum of white light emitting diode combining blue LED with borate-based yellow phosphor in accordance with one  
15 embodiment of the present invention. As shown in FIG.3, the yellow phosphor absorbs a portion of light emitted from the blue LED chip and emits a second light of wavelength different from that of the absorbed light. Thus, the combination of the second light and the reference light produces white light.

The semiconductor light emitting device in accordance with the present  
20 invention comprises a semiconductor LED, and a phosphor coating layer including a transparent resin, and the yellow phosphor which absorbs a portion of light emitted from the blue LED and emits light of wavelength different from that of the absorbed light. The yellow phosphor is one represented by the Chemical formula 1,

The main emission spectrum peak of the LED lies in the range from 400 nm to 530 nm. Preferably, the main emission wavelength of the yellow phosphor is longer than the main peak wavelength of the nitride semiconductor.

The semiconductor LED can be a UV chip or a blue chip comprising GaN, 5 InGaN, or AlGaN nitride phosphor coated on sapphire, SiC, or other materials as substrates. The main emission spectrum peak of the LED lies in the range from 400 nm to 530 nm. Preferably, the main emission wavelength of the yellow phosphor is longer than the main peak wavelength of the nitride semiconductor.

The shape of yellow phosphor in the phosphor coating layer is not limited, 10 but is preferably spherical or flaked. Preferably, the particle size of the yellow phosphor ranges from 0.1 to 50  $\mu\text{m}$ .

Based on the fact that the phosphor emits light on its surface, as the particle size of the phosphor decreases, the emission intensity increases because of large surface area. However, if the particle size is excessively reduced, scattered 15 light is absorbed by the particle, and thus disappears. Thus, to maximize the emission property of the phosphor, phosphor with the optimal particle size is needed.

The specific gravity of fluorescent material is several times as high as the coating solution before it is cured. The viscosity of thermosetting resin decreases 20 greatly when it is cured by heat. Thus, if the LED chip is coated with liquid resin including phosphor, most of the phosphor in the resin solution tends to sediment and to congregate around the LED chip. Because only the phosphor precipitated around the LED chip absorb the emitted light effectively, most phosphor cannot

convert the light emitted from LED chip, and block the light emitted by the phosphor, so as to reduce the light energy. As a result, this can reduce the emission intensity of the light emitting diode.

Considering the surface emitting property of the phosphor, in the present 5 invention, the phosphor with the suitable particle size, shape, and distribution is used so that the phosphor can show as many light conversion as possible.

If the mean diameter of the phosphor exceeds 50  $\mu\text{m}$ , a large amount of the phosphor is needed because of a low surface area. Thus, this causes light blocking and reduces the light energy at high extent. If the mean diameter of the phosphor is 10 less than 100 nm, the light emission property decreases rapidly in the process of reducing the particle size of the preparation of the phosphor. According to one embodiment of the present invention, the amount of the phosphor with mean diameter ranging from 100 nm to 1  $\mu\text{m}$  is 0.01 to 10 wt% with respect to the total amount of the phosphor, and the phosphor with mean diameter of 1 to 50  $\mu\text{m}$  is the 15 remaining amount. In addition, the color conversion layer can be formed effectively by locating the phosphor with large mean diameter in a lower part of the phosphor coating layer and the phosphor with small mean diameter in a higher part.

In consideration of the color rendering of the light emitting device, and light blocking and energy reduction caused by the phosphor, the relations between the 20 thickness of phosphor coating layer( $T_1$ ) and the thickness of the LED ( $T_2$ ) satisfies the formula,  $T_2 < T_1 \leq 3T_2$ , more preferably  $1.5 T_2 < T_1 \leq 2.5T_2$ .

For the transparent resin used in the phosphor coating layer, any resin

available in the art for such purpose can be used. Preferably, an epoxy resin or a silicone resin is used.

The phosphor coating layer may further comprise a zinc selenium-based red phosphor. The amount of zinc selenium-based red phosphor depends on the 5 color range to be implemented. Preferably, the zinc selenium-based red phosphor is contained in 10 to 40 wt%, more preferably 10 to 20 wt%, based on the weight of the yellow phosphor. If the amount of the zinc selenium-based red phosphor increases, the pink light is more implemented.

FIG. 4 shows an absorption spectrum and an emission spectrum of the zinc 10 selenium-based red phosphor. As shown in FIG.4, the absorption spectrum shows a high absorption peak at 400 to 530 nm region, and the emission spectrum shows a high emission peak at about 620 nm. Accordingly, the zinc selenium-based red phosphor can be effectively used for implementing red light in combination with a UV chip and pink light in combination with a blue chip, and for 15 applications adopting the light as an energy source.

FIG. 5 shows an emission spectrum of a pink emitting diode combining a zinc selenium-based red phosphor with a blue LED. As shown in FIG.5, the red phosphor absorbs a portion of light emitted from the blue LED chip and emits a second light of wavelength different from that of the absorbed light, thus the 20 combination of the second light and the reference light produces white or pink light.

FIG. 6 is a color coordinate showing the colorization range that can be obtained by a light emitting diode combining a borate-based yellow phosphor, a zinc selenium-based red phosphor and a blue LED. As shown in FIG.6, colors belonging to the color coordinate can be obtained by selecting the blue chip in the

range from 450 to 480 nm, and controlling the mixing ratio of the terbium borate-based yellow phosphor and zinc selenium-based red phosphor.

The light emitting diode according to an embodiment of the present invention has a high energy band gap in the light emitting layer. The light emitting device is formed by combining a blue semiconductor InGaN based LED, borate-based yellow phosphor, and zinc selenium-based red phosphor. White, bluish white, pink, and pastel tone color can be implemented by a combination of blue light from the blue LED, and yellow and red color emitted from the phosphor which is excited by the light emitted from the blue LED. In addition, the white semiconductor light emitting device of the present invention offers a greatly improved color rendering and experiences less deterioration in light emission efficiency over a long period of service.

The white semiconductor light emitting device of the present invention can be fabricated in a surface mount type or a lead type during the packaging process. Such materials as metal stem, lead frame, ceramic, printed circuit board, etc. can be used for packaging. The packaging is performed to protect the device from electrical connection with outside and from external mechanical, electric and environmental factors, to offer a heat dissipation path, increase the light emission efficiency, optimize orientation, and so forth.

FIG.7 to 11 show a variety of white semiconductor light emitting device.

FIG.7 is a schematic view of lead type white semiconductor light emitting device, and a partial enlarged sectional view thereof. The lead type white semiconductor light emitting device comprises a cup-shaped recess portion 9 on top of a lead frame, an LED chip 3 and a phosphor coating layer 6 at the recess

portion 9. The LED chip 3 is connected to an anode lead 4 and a cathode lead 5 by metal wires 1, 2. A portion of the anode lead 4 and cathode lead 5 is exposed to outside and all the other components are sealed in a casing 7 made of transparent or colored light-transmitting material. The inner wall of the recess portion 9 acts 5 as a reflection plate, and the phosphor coating layer 6 comprises yellow phosphor particles 8 and a transparent epoxy resin or silicone resin.

FIG.8 is a schematic view of a double mold type white semiconductor light emitting device, and a partial enlarged sectional view thereof. FIG.8 is different from the FIG. 7 in that mold material is formed in a dual-layer in the recess portion 9. 10 That is, transparent material layer 6b, such as a silicone layer is formed to the upper part which is higher than the top of LED chip 3 in the recess portion, while covering the LED chip 3, and the phosphor coating layer 6a is formed on the transparent material layer. Considering a depth of the recess portion 9 is about 0.2–0.6 mm and a height of the blue LED chip 3 is about 100  $\mu\text{m}$ , it is preferable that a thickness 15 of the transparent material layer 6b is about 100–200  $\mu\text{m}$ . The phosphor coating layer 6a is formed on the transparent material layer 6b while covering an upper portion of the recess portion 9.

FIG.9 is a schematic view of a surface mount type white light emitting device of the reflector injection type.

20 As shown in FIG.9, the light emitting device comprises a casing 16 with recess portion 17 in its upper part, and metal ends 11, 12 which acts as the anode lead and the cathode lead. The anode and cathode lead 11 and 12 are respectively

connected to N-type and P-type electrodes of the LED chip 10 by fine metal wires

14. A phosphor coating layer containing the transparent resin and phosphor particle is on the LED chip 10 disposed in an inner part of the recess portion 17. The molding layer 15 is at the same height as the top of the recess portion 17, so that

5 the metal wire is embedded in the molding layer. The inner wall of the recess portion 17 acts as a reflective plate, and the recess portion 17 can be formed by injection molding. The phosphor coating layer includes a yellow phosphor, and can further include a red phosphor.

FIG.10 is a schematic view of white light emitting device of the reflector

10 injection type and double mode structure. The embodiment is different from those of FIG. 7 and FIG.9 in that the recess portion 17 has triple mold layers. That is, a transparent material layer 13b is formed to the upper part which is higher than the top of the LED chip 10, while covering a top surface of the LED chip 10 in inner part of the recess portion 17. A phosphor coating layer 13a is formed on the

15 transparent material layer 13b, and another transparent material layer is formed on the phosphor coating layer 13a at the same height as the top surface of the recess portion 17. The structure can be obtained by forming a transparent material layer 13b such as silicone on a bottom of the cut while covering a top surface of the LED chip 10, filling a liquid molding material containing a yellow phosphor and zinc

20 selenium-based red phosphor on the transparent material layer to precipitate uniformly the phosphor as a phosphor coating layer 13a based on the specific gravity difference between the phosphor and molding material, forming the

transparent molding layer 15 in the inner part of the recess portion 17.

According to the present invention, the white light emitting diode with high energy efficiency can be obtained by controlling the particle size of the phosphor to be sub-micrometer, the shape and distribution of the phosphor to locate in the inner part of the recess portion, and the height of the phosphor coating layer. That is, the color conversion layer is efficiently formed by distributing the phosphor particles from small particle size of sub-micrometer in the bottom to large particle size in the top of the recess portion. Thus, the white light emitting diode with the high efficiency can be obtained by controlling the thickness of the filling layer.

10 FIG.11 is a sectional view of a surface mount type white light emitting device of the PCB type. As shown in FIG.11, the LED chip 20 is on the PCB layer 25, and the anode and cathode lead 22 and 21 are respectively connected to N-type and P-type electrodes of the LED chip 10 via fine metal wires 24. The phosphor coating layer 23, and a molding layer 26 are located on the LED chip 20 in order.

15 The phosphor coating layer 23 comprises the transparent resin and yellow phosphor.

When a height of the blue LED chip is 100  $\mu\text{m}$ , the thickness of the yellow phosphor coating layer is 100 to 300  $\mu\text{m}$ , which is one to three times as high as the height of the LED chip mounted in the recess portion. More preferably, the 20 thickness of the yellow phosphor coating layer is 150  $\mu\text{m}$  to 250  $\mu\text{m}$ . If the thickness of the yellow phosphor coating layer is less than 100  $\mu\text{m}$ , the insufficient coating on the surface of the LED chip makes it difficult to implement white color. If it exceed

300  $\mu$ m, the light blocking and low energy efficiency reduce the light emitting property of the semiconductor light emitting device.

In accordance with the present invention, the yellow phosphor is applied in the upper side of the semiconductor light emitting device including the nitride semiconductor by selectively combining zinc selenium based red phosphor, and thus, white, blue-white, pink and pastel colors can be obtained by combining blue light of the semiconductor light emitting device, yellow light emitted by the yellow phosphor exposed to the blue light, and selectively red light of the red phosphor.

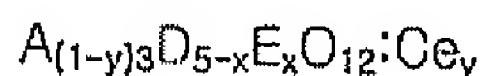
While the present invention has been described in detail with reference to the preferred embodiments, those skilled in the art will appreciate that various modifications and alterations can be made thereto without departing from the spirit and scope of the present invention as set forth in the appended claims.

As described above, a white, bluish white, pink, and pastel color light emitting diode containing the yellow phosphor and zinc selenium based red phosphor of the present invention absorbs a portion of light in the long wavelength UV region and in the visible light region emitted from a light emitting diode and emits yellow and red light. Therefore, it can be applied for red light emitting diode for a UV LED, white light emitting diode for blue, bluish white, pastel color, and pink light emitting diode, and such LED fields in which light of long wavelength UV and blue region is used as an energy source. Particularly, it is suitable as a back light source of LCDs since it has superior emission luminance and color rendering.

WHAT IS CLAIMED IS:

1. A yellow phosphor represented by the following chemical formula 1:

Chemical formula 1



5 wherein, A is at least one element selected from the group consisting of Y, Lu, Sc, La, Gd and Sm; D is at least one element selected from the group consisting of Al, Ga and In; E is at least one element selected from the group consisting of B and Fe;  $0 \leq x < 0.5$ ; and  $0.0001 \leq y < 0.5$ .

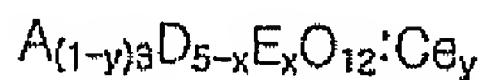
2. The yellow phosphor according to claim 1, wherein the phosphor has an absorption peak ranging from about 420 nm to 480 nm, and an emission peak ranging from about 510 nm to 570 nm.

3. The yellow phosphor according to claim 1, wherein A is a mixture of Y and Gd.

4. The yellow phosphor according to claim 1, wherein the phosphor is of a spherical shape, and its mean diameter is 100 nm to 50  $\mu m$ .

5. A white semiconductor light emitting device comprising:  
a semiconductor light emitting diode; and  
a phosphor coating layer comprising a yellow phosphor which absorbs a portion of light emitted by the semiconductor light emitting diode and emits light of  
20 wavelength different from that of the absorbed light and a transparent resin, where the yellow phosphor is represented by the following chemical formula 1:

Chemical formula 1



wherein: A is at least one element selected from the group consisting of Y, Lu, Sc, La, Gd and Sm; D is at least one element selected from the group consisting of Al, Ga and In; E is at least one element selected from the group consisting of B and Fe;  $0 \leq x < 0.5$ ; and  $0.0001 \leq y < 0.5$ ;

5 6. The white semiconductor light emitting device according to claim 5, wherein the thickness of the phosphor coating layer ( $T_1$ ) and the thickness of the semiconductor light emitting diode ( $T_2$ ) meet the formula of  $T_2 < T_1 \leq 3T_2$

7. The white semiconductor light emitting device according to claim 5, wherein the yellow phosphor contains phosphor with mean diameter of less than 1  $\mu\text{m}$  in an amount of 0.01 to 10 wt%, and phosphor with mean diameter of 1 to 50  $\mu\text{m}$  in an amount of 90 to 99.9 wt%.

8. The white semiconductor light emitting device according to claim 5, wherein the phosphor coating layer comprises a lower part including the phosphor with mean diameter of 1 to 50  $\mu\text{m}$ , and an upper part including the phosphor with 15 mean diameter of less than 1  $\mu\text{m}$ .

9. The white semiconductor light emitting device according to claim 5, wherein the phosphor coating layer further comprises a zinc selenium-based red phosphor.

10. The white semiconductor light emitting device according to claim 9, 20 wherein the amount of the zinc selenium-based red phosphor is 10 to 40 wt% based on the weight of the yellow phosphor.

11. The white semiconductor light emitting device according to claim 5, wherein the semiconductor light emitting diode comprises a substrate and a nitride

semiconductor layer on the substrate.

12. The white semiconductor light emitting device according to claim 11, wherein the substrate is made of sapphire or silicone carbide.

13. The white semiconductor light emitting device according to claim 11, 5 wherein the nitride semiconductor layer includes a GaN, InGaN, or AlGaN-based semiconductor.

14. The white semiconductor light emitting device according to claim 5, wherein the transparent resin is a transparent epoxy resin or a silicone resin.

15. A lead type white semiconductor light emitting device comprising:  
10 a mount lead comprising a lead and a recess portion on the lead;  
a UV or a blue LED chip which is disposed in the recess portion, and an anode, a cathode of which are connected to the lead of the mount lead by metal wires;  
a phosphor coating layer filled inside the recess portion to cover the LED  
15 chip; and

a casing that seals the mount lead excluding lower portions of the mount lead, the LED chip and phosphor coating layer,

wherein the phosphor coating layer comprises a transparent resin and a yellow phosphor represented by the following chemical formula 1:

20 Chemical formula 1



Wherein, A is at least one element selected from the group consisting of Y, Lu, Sc, La, Gd and Sm; D is at least one element selected from the group consisting of Al, Ga and In; E is at least one element selected from the group consisting of B and Fe;

0 ≤ x < 0.5; and 0.0001 ≤ y < 0.5;

16. The white semiconductor light emitting device according to claim 15, wherein the phosphor coating layer further comprises a zinc selenium-based red phosphor.

5 17. The white semiconductor light emitting device according to claim 15, which further comprises a transparent material layer between the semiconductor light emitting diode and the phosphor coating layer.

10 18. A lead type white semiconductor light emitting device comprising:  
a casing with a recess portion on its top, and metal lead;  
a UV and a blue LED chip which is disposed in the recess portion, and an anode and a cathode of which are connected to the lead by metal wires; and  
a phosphor coating layer filled inside the recess portion to cover the LED chip,

15 wherein, the phosphor coating layer comprises a transparent resin and a yellow phosphor represented by the following chemical formula 1,

Chemical formula 1



wherein: A is at least one element selected from the group consisting of Y, Lu, Sc, La, Gd and Sm; D is at least one element selected from the group consisting of Al, 20 Ga and In; E is at least one element selected from the group consisting of B and Fe;

0 ≤ x < 0.5; and 0.0001 ≤ y < 0.5;

19. The white semiconductor light emitting device according to claim 18, wherein the phosphor coating layer further comprises a zinc selenium-based red

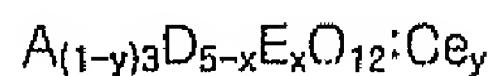
phosphor.

20. The white semiconductor light emitting device according to claim 18, which further comprises a transparent material layer between the semiconductor light emitting device and the phosphor coating layer.

5 21. The white semiconductor light emitting device according to claim 18, which further comprises a transparent molding layer on the phosphor coating layer.

22. A surface mount type white semiconductor light emitting device of the PCB (printed circuit board) type comprising a UV and blue LED chip and a phosphor coating layer formed orderly on a PCB layer, wherein the phosphor 10 coating layer comprises yellow phosphor represented by the following chemical formula 1:

Chemical formula 1



Wherein, A is at least one element selected from the group consisting of Y, Lu, Sc, 15 La, Gd and Sm; D is at least one element selected from the group consisting of Al, Ga and In; E is at least one element selected from the group consisting of B and Fe; 0 ≤ x < 0.5; and 0.0001 ≤ y < 0.5;

23. The white semiconductor light emitting device according to claim 22, wherein the phosphor coating layer further comprises a zinc selenium-based red 20 phosphor.

24. The white semiconductor light emitting device according to claim 22, which further comprises a transparent molding layer on the phosphor coating layer.

25. A liquid crystal display incorporating the white semiconductor light

emitting device according to any one of claims 5 to 14 as a back light source.

FIG. 1

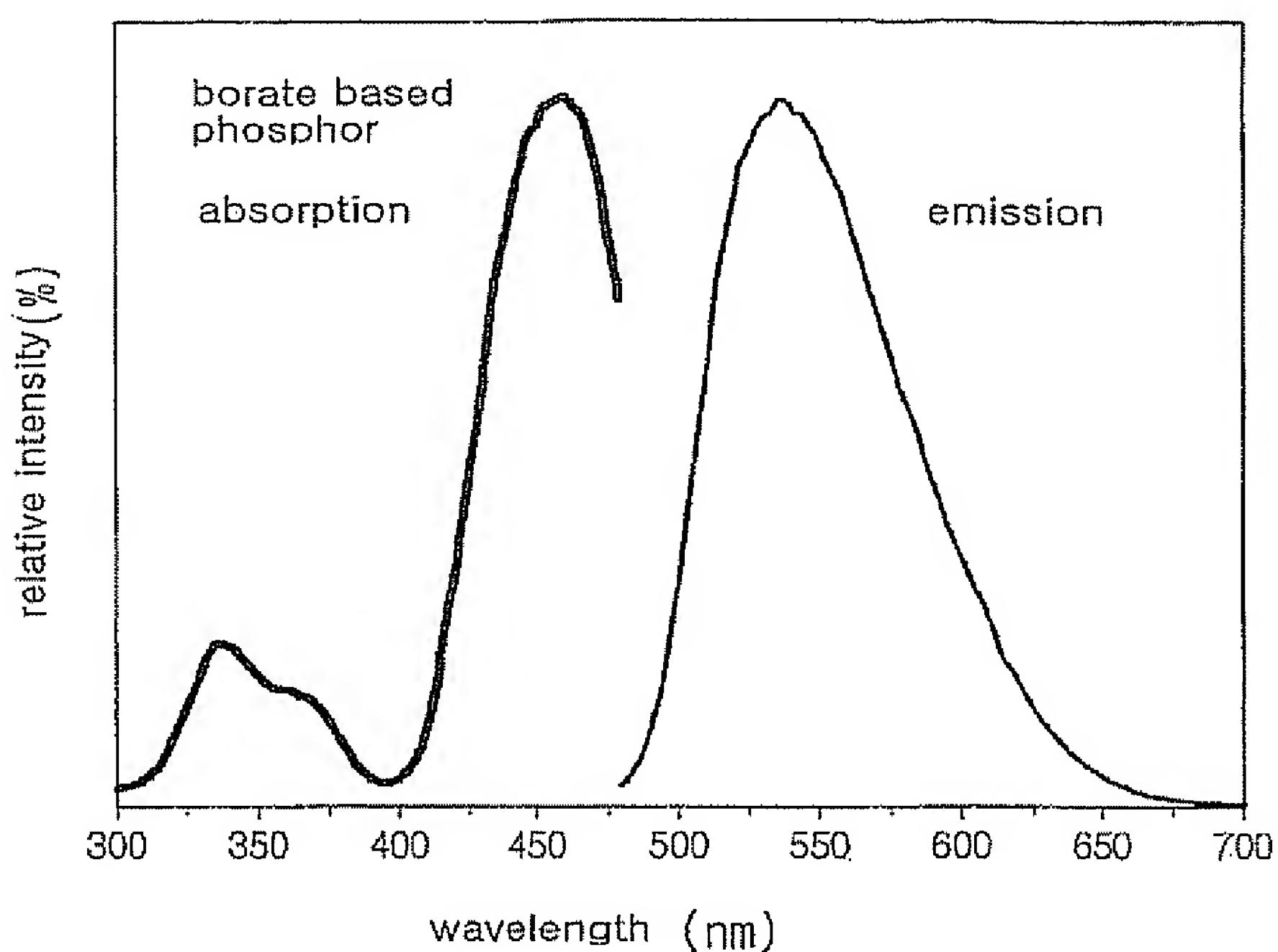


FIG. 2

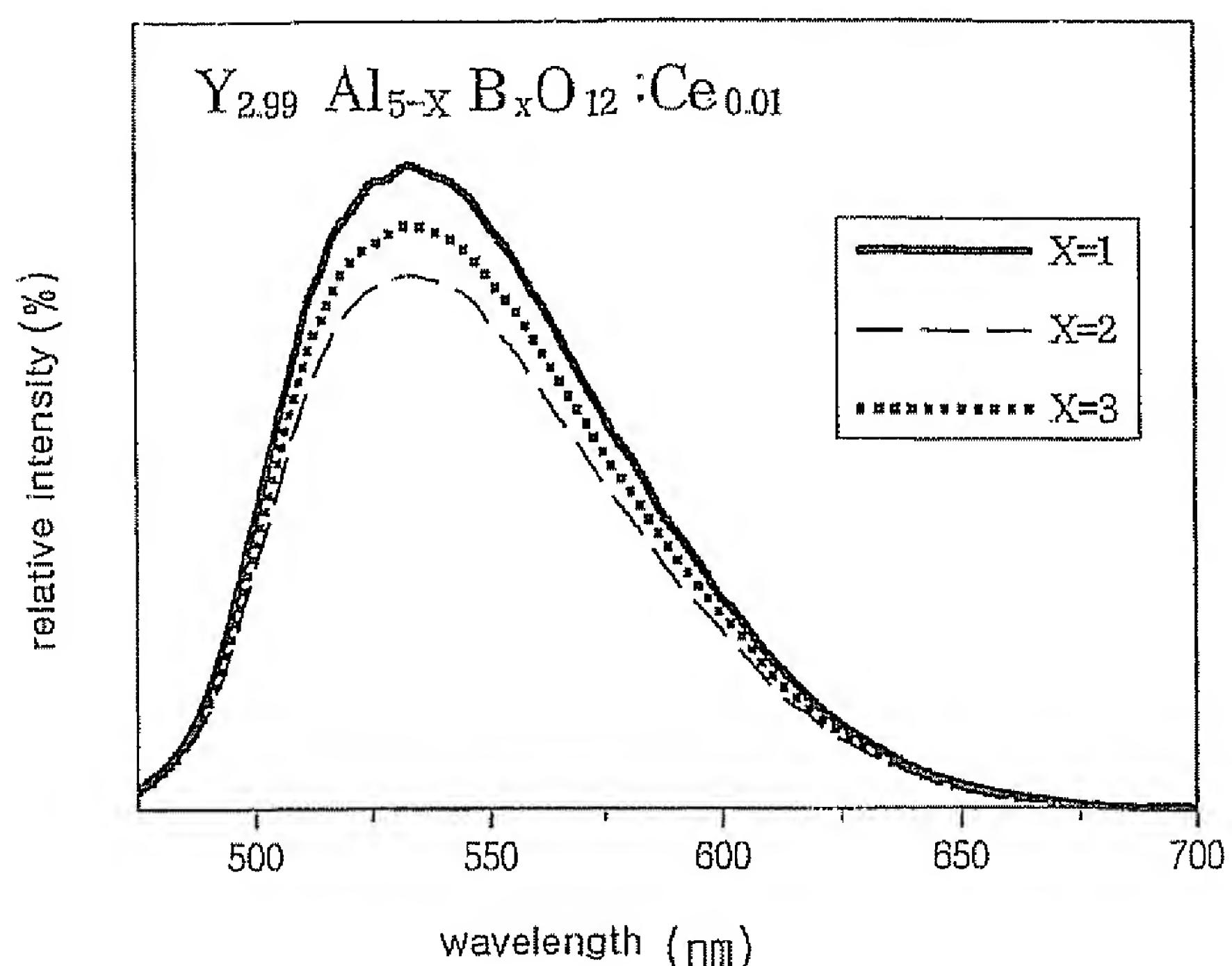


FIG. 3

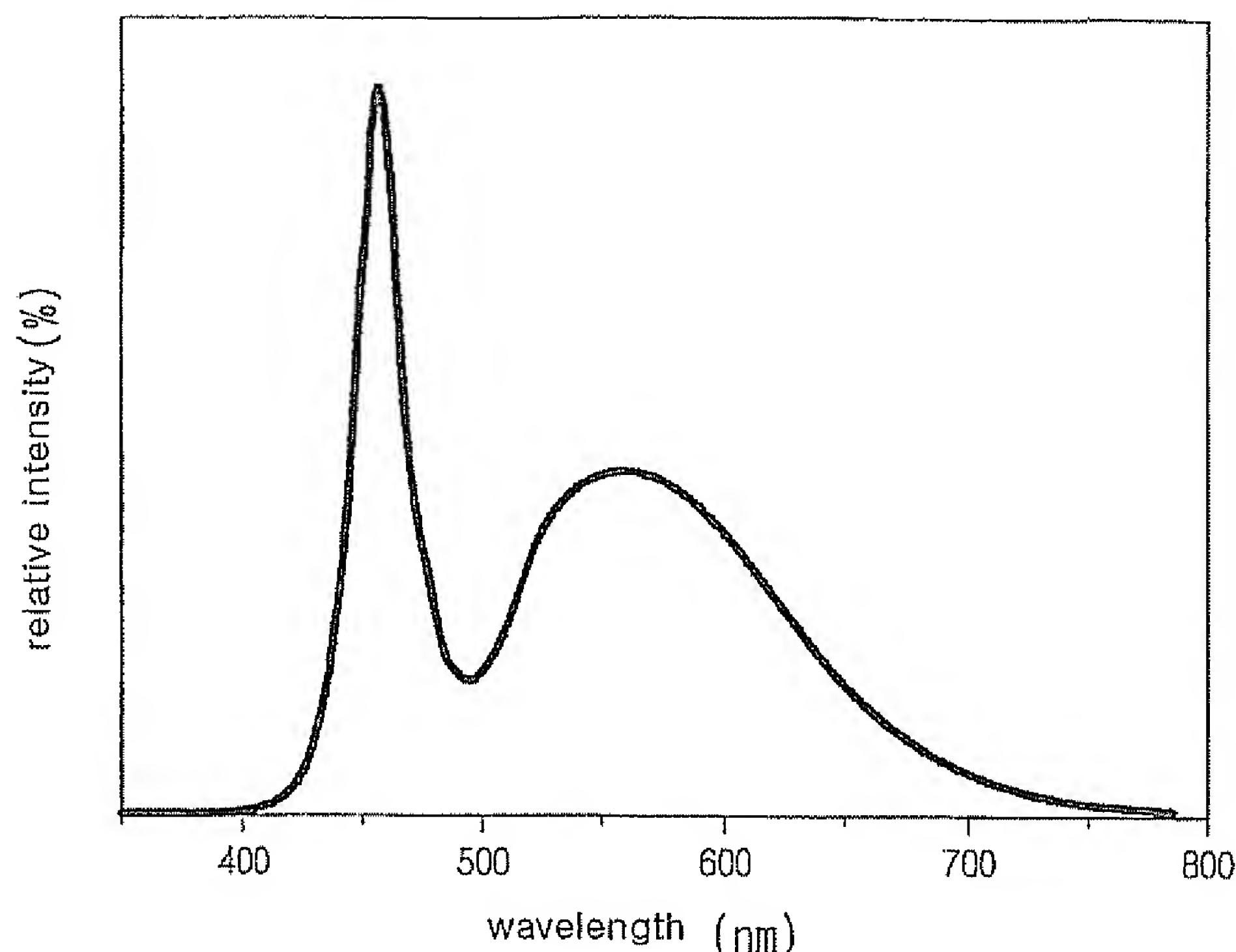


FIG. 4

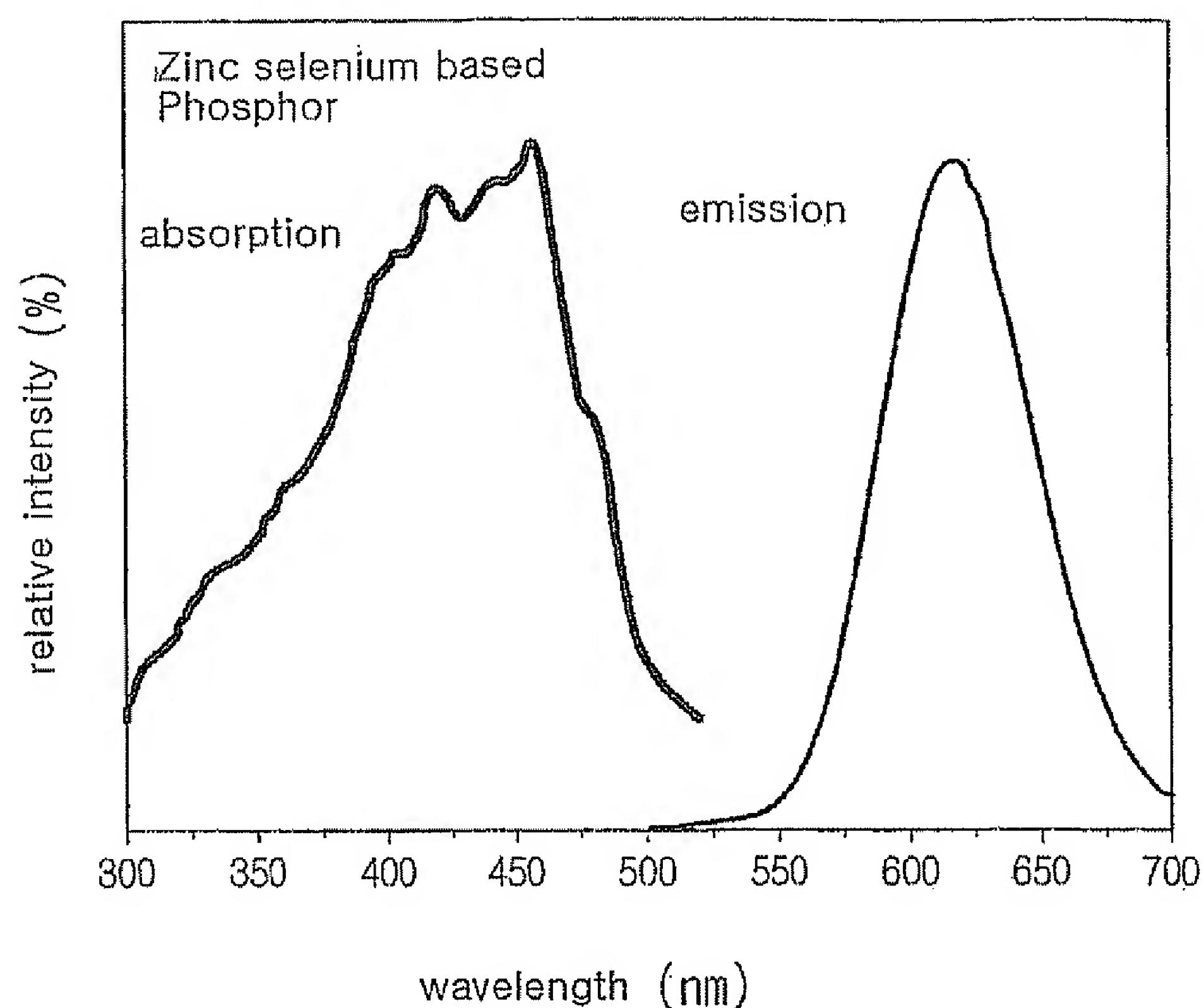


FIG. 5

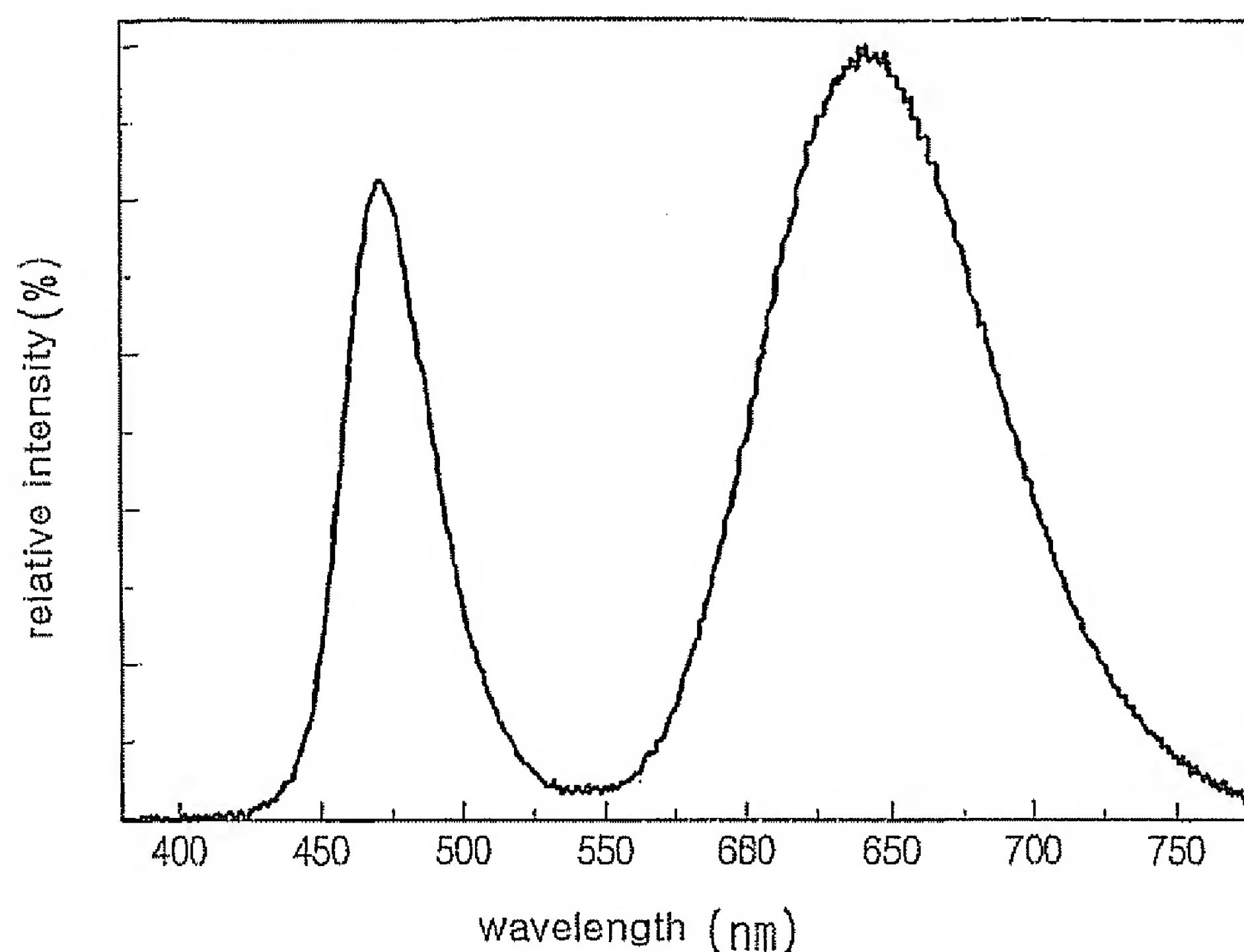


FIG. 6

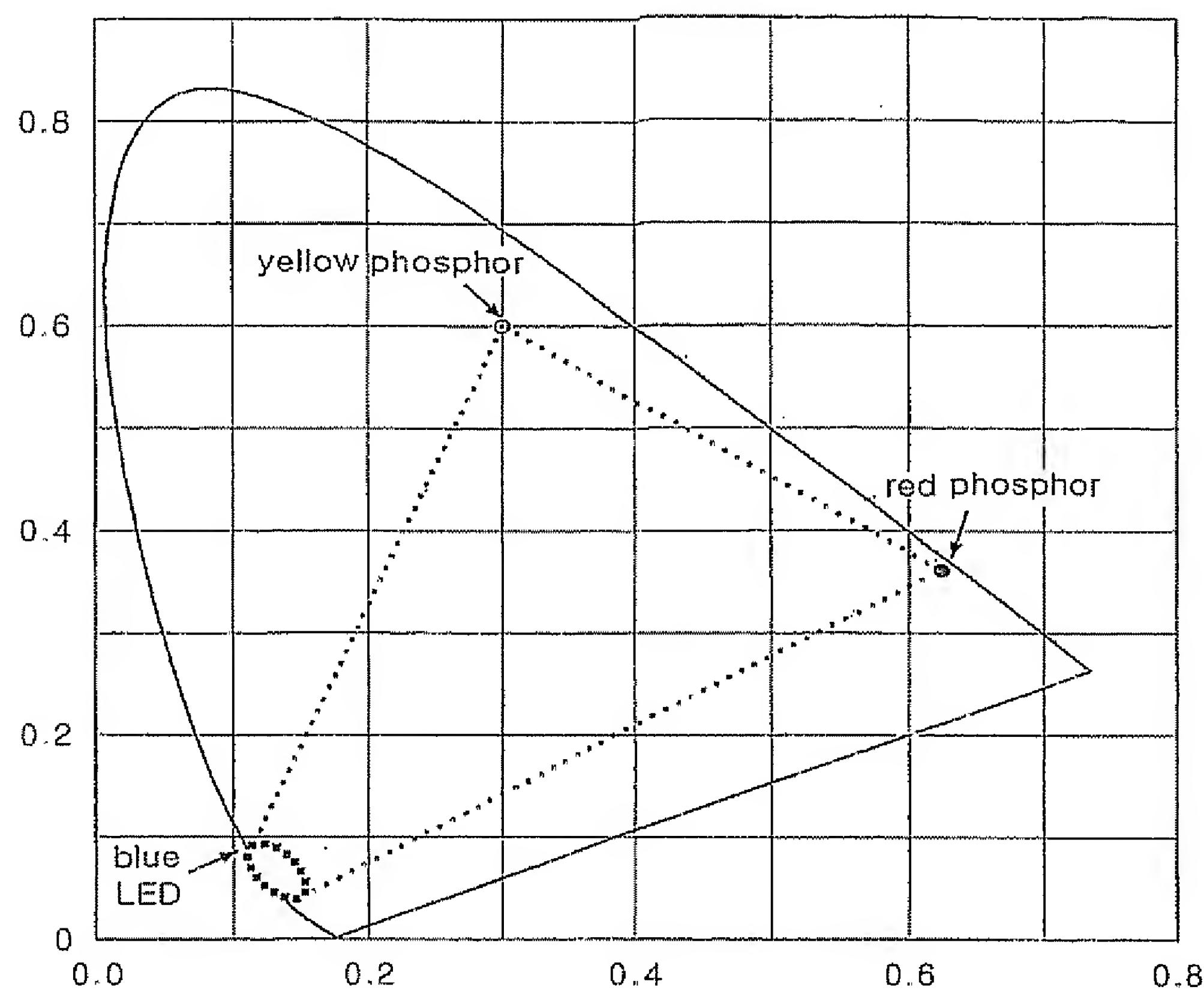


FIG. 7

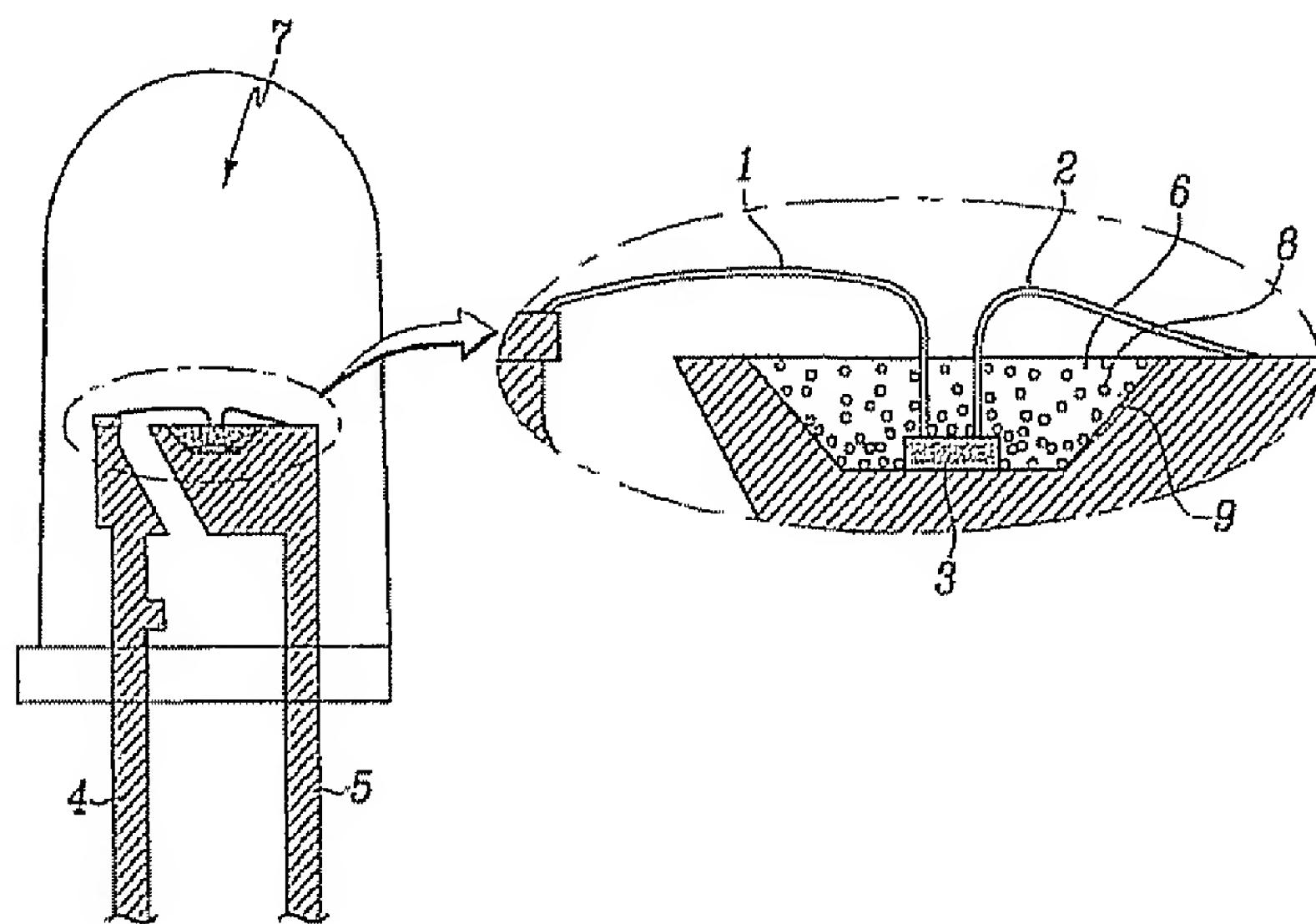
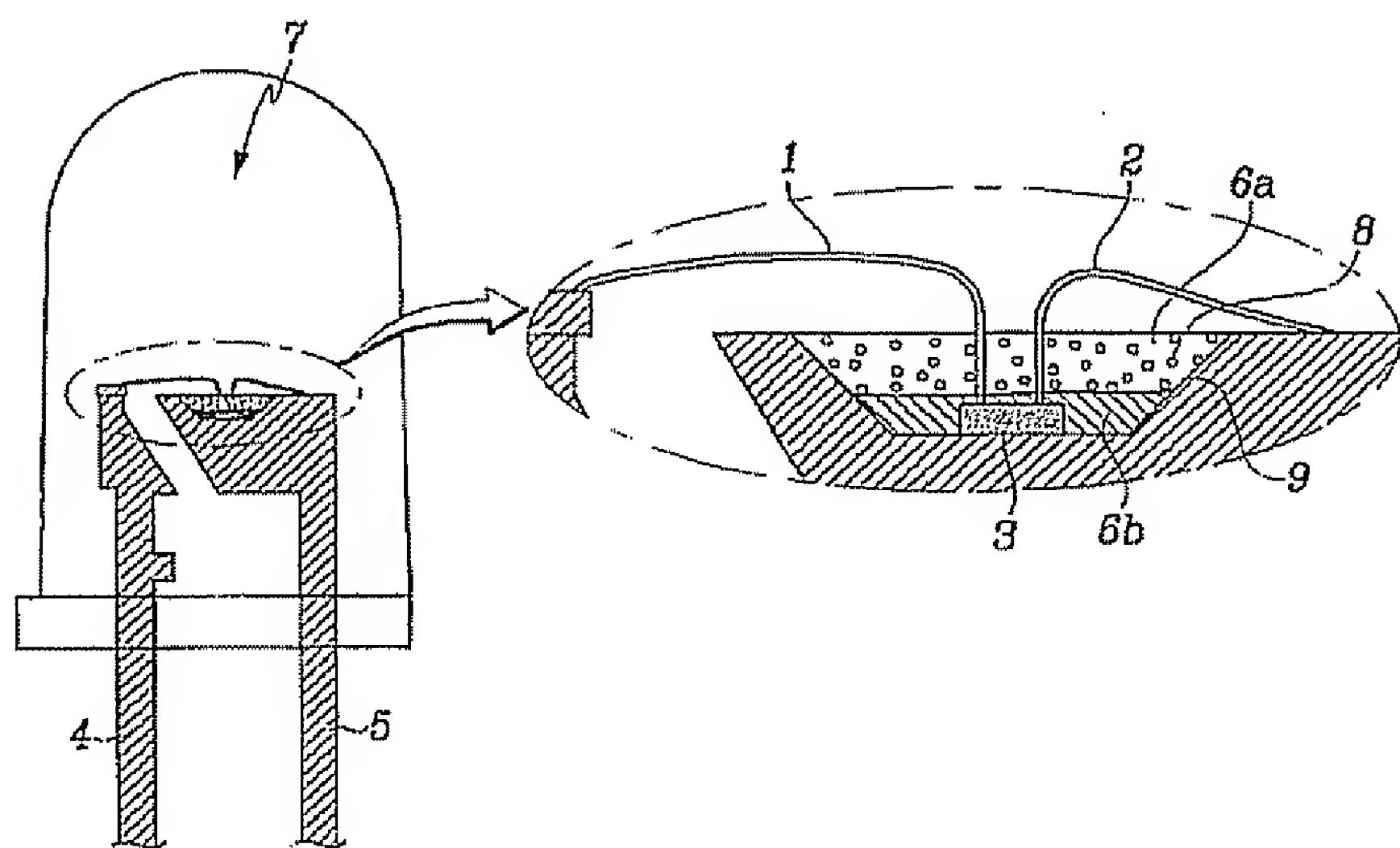


FIG. 8



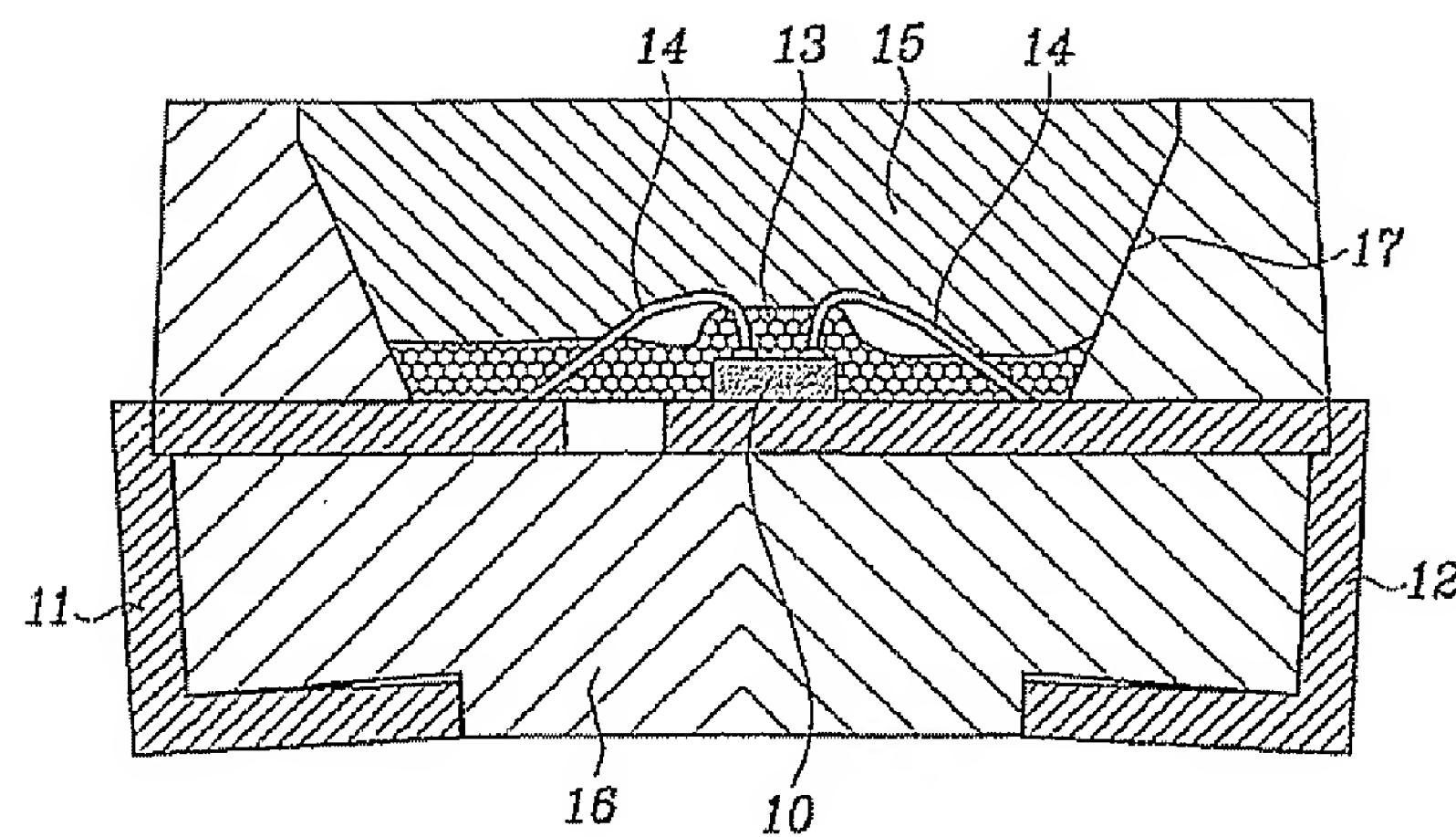
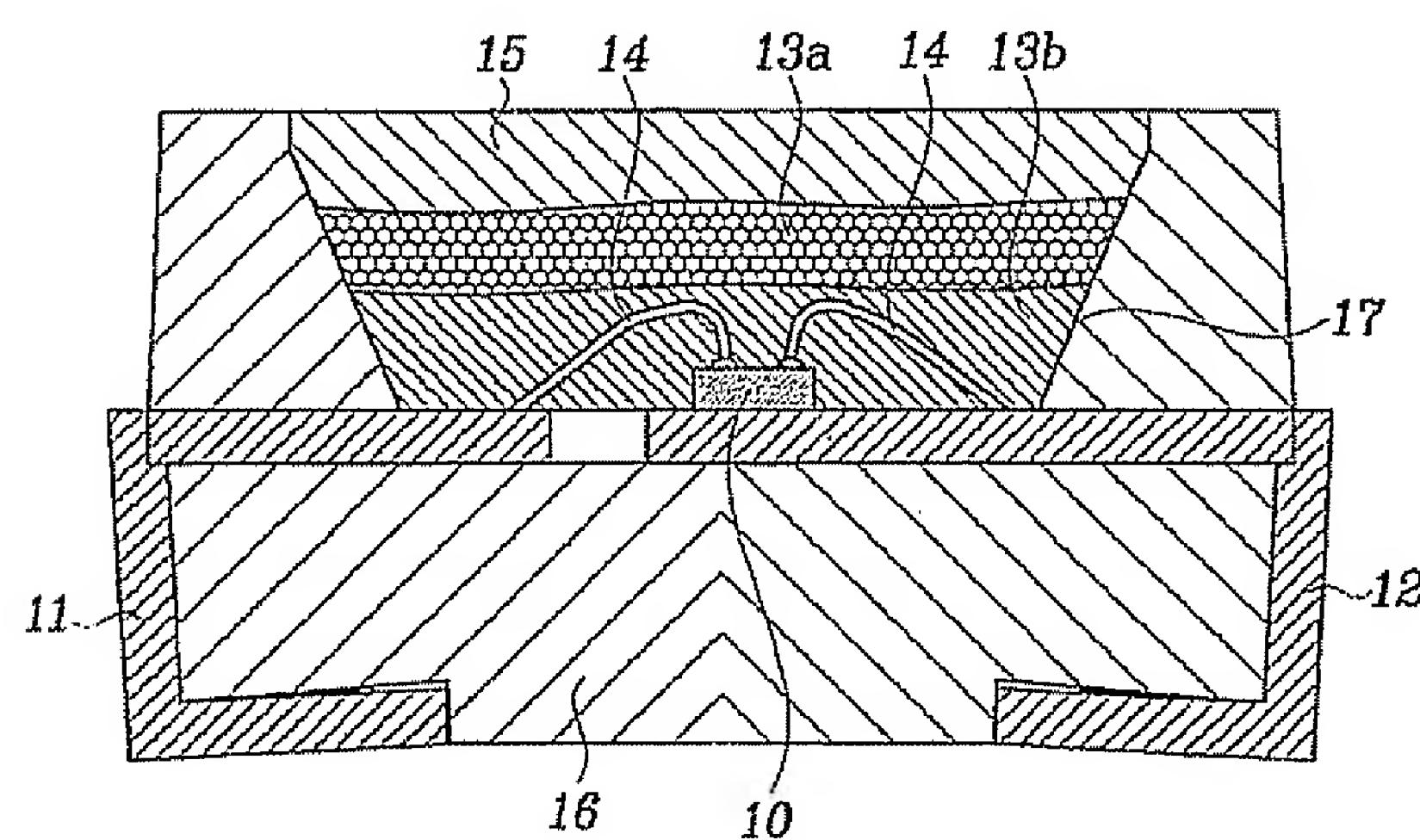
**FIG. 9****FIG. 10**

FIG. 11

